



Job Description – Foundry Interface Manager [MPH046] Meyreuil (Aix-en-Provence), France

Position Summary:

Based in Meyreuil (located near Aix-en-Provence, South of France), you will be managing and implementing the Company's foundry sourcing and partnership activities as a key member of our Industrial Manufacturing Organization. As Foundry Interface Manager position, you will be required to work with both customer and Wafer Fabs seamlessly from NPI (new product introduction) to high volume mass production sustaining supporting our Supply Chain Management team along the Customer's Service life cycle.

As a single point of contact for our Front-end Manufacturing Partners you will be in charge of wafer fab technical Tape-Out execution from Multi Project Wafer (MPW) projects setup to Single Layer Reticle Production release through our B2B interfaces. On major turn-key production projects you will be serving as the customer's advocate in driving resolution of issues and continuous improvement collaborating closely with the Company's Device and Product Engineering Team.

As a foundry technology field expert, you will be working jointly with the Sales & Marketing department to refine and meet Customer's Finished Product price point and technology node selection.

As a turn-key production service company, travels are needed to support our customers or our suppliers. You will have opportunities for development into either mentorship/supervision roles or by expanding your knowledge spectrum and technical contribution scope.

Responsibilities:

- Participate to the Customer's project feasibility phase, including process baseline definition, IP selection and availability and wafer layer costing.
- Support purchasing with Foundry wafer cost analysis, and cost erosion roadmap.
- Drive and Control Product Creation into foundry portal and Company's B2B interfaces
- Manage and/or execute all tasks related to Tape-out support from foundry portal usage to actual Data-Prep submission.
- Provide technical guidance and expertise on CMOS & III/V silicon process diffusion, Design for Manufacturing, and Design for Yield
- Project management & technical reporting



Requirements

- Minimum MS, PhD preferred in Physics or Electrical Engineering (Bac + 5)
- Experience (5+ year) in front-end fab, process engineering, or similar roles the industry
- Strong expertise in silicon diffusion process, reliability, yield, Quality Analytical Methods.
- Demonstrated history of productive and successful interactions with a wide variety of internal stakeholders (technology, engineering, procurement, finance, costing, supply chain).
- Very good inter-personal skills, enthusiasm and drive, Customer Relation Management experience an asset
- Willingness to be flexible and accept new challenges
- Verbal and written English, Chinese Verbal and written an asset